

# Micron engineering candidate guide

Micron is looking for the brightest minds to join our international team of innovators. Match your education and experience to see what jobs might be a fit for you.



Department	Job title(s)	Education	Degree	Preferred coursework	Job location(s)	
<b>Process Development/ Equipment R&amp;D</b>	<ul style="list-style-type: none"> <li>Process development engineer</li> <li>Process pathfinding engineer</li> <li>Equipment R&amp;D engineer</li> <li>Areas: Photolithography, Dry etch, Wet process, CMP, Thin films, Implant, Metrology, and RDA</li> </ul>	<ul style="list-style-type: none"> <li>MS</li> <li>PhD</li> </ul>	<ul style="list-style-type: none"> <li>Chemistry/Chemical engineering</li> <li>Electrical engineering</li> <li>Microelectronics engineering</li> <li>Materials science</li> <li>Physics</li> <li>Mechanical engineering</li> <li>Mechatronics</li> </ul>	<ul style="list-style-type: none"> <li>Device or processing research</li> <li>Semiconductor processing lab</li> <li>Data science application in engineering</li> <li>Statistics</li> <li>Design of experiments (DOE)</li> </ul>	<ul style="list-style-type: none"> <li>Computational fluid dynamics</li> <li>Computational materials</li> <li>Thermo-mechanical modeling</li> <li>AI assisted modeling</li> <li>Optics</li> </ul>	<ul style="list-style-type: none"> <li>Boise, ID</li> </ul>
<b>Process Integration R&amp;D</b>	<ul style="list-style-type: none"> <li>Process integration engineer</li> <li>Device engineer</li> <li>CMOS engineer</li> </ul>	<ul style="list-style-type: none"> <li>MS</li> <li>PhD</li> </ul>	<ul style="list-style-type: none"> <li>Chemistry/Chemical engineering</li> <li>Electrical engineering</li> <li>Microelectronics engineering</li> <li>Materials science</li> <li>Physics</li> </ul>	<ul style="list-style-type: none"> <li>Semiconductor physics</li> <li>Device or processing research</li> <li>CMOS</li> <li>Design of experiments (DOE)</li> </ul>	<ul style="list-style-type: none"> <li>SPC, JMP</li> <li>Circuit design</li> <li>Matlab, SQL, Python, Java, or other data science coursework</li> </ul>	<ul style="list-style-type: none"> <li>Boise, ID</li> </ul>
<b>Mask Technology Center</b>	<ul style="list-style-type: none"> <li>Process development engineer</li> <li>Reticle manufacturing engineer</li> <li>R&amp;D process engineer</li> <li>Process integration engineer</li> <li>Yield engineer</li> </ul>	<ul style="list-style-type: none"> <li>BS</li> <li>MS</li> <li>PhD</li> </ul>	<ul style="list-style-type: none"> <li>Chemistry/Chemical engineering</li> <li>Mechanical engineering</li> <li>Materials science</li> <li>Mathematics/statistics</li> <li>Physics</li> </ul>	<ul style="list-style-type: none"> <li>Semiconductor physics</li> <li>Statistics</li> <li>Optics</li> </ul>	<ul style="list-style-type: none"> <li>SPC, Design of experiments (DOE), JMP</li> <li>C++, SQL, Python, Java</li> </ul>	<ul style="list-style-type: none"> <li>Boise, ID</li> </ul>
<b>Central Engineering</b>	<ul style="list-style-type: none"> <li>CAD engineer</li> <li>Competitor analysis engineer</li> <li>EMI/EMC engineer</li> <li>Lab engineer</li> <li>PCB design &amp; test engineer</li> <li>Signal integrity engineer</li> <li>Data scientist</li> </ul>	<ul style="list-style-type: none"> <li>BS</li> <li>MS</li> <li>PhD</li> </ul>	<ul style="list-style-type: none"> <li>Chemistry/Chemical engineering</li> <li>Computer engineering</li> <li>Computer engineering</li> <li>Electrical engineering</li> <li>Mechanical engineering</li> <li>Thermal engineering</li> <li>EHSQ</li> <li>Materials science</li> <li>Physics</li> </ul>	<ul style="list-style-type: none"> <li>Device physics</li> <li>EDA</li> <li>Electromagnetics</li> <li>Electron microscopy</li> <li>Lab testing equipment</li> <li>Mechanical understanding of tools</li> </ul>	<ul style="list-style-type: none"> <li>PCB design</li> <li>VLSI design</li> <li>Programming</li> <li>Statistics</li> </ul>	<ul style="list-style-type: none"> <li>Atlanta, GA</li> <li>Austin, TX</li> <li>Dallas, TX</li> <li>Boise, ID</li> <li>Folsom, CA</li> <li>San Jose, CA</li> <li>Longmont, CO</li> <li>Minneapolis, MN</li> </ul>
<b>Global Quality</b>	<ul style="list-style-type: none"> <li>Manufacturing quality engineer</li> <li>Quality data analyst engineer</li> <li>Quality product reliability engineer</li> </ul>	<ul style="list-style-type: none"> <li>BS</li> <li>MS</li> <li>PhD</li> </ul>	<ul style="list-style-type: none"> <li>Computer engineering</li> <li>Electrical engineering</li> <li>Mechanical engineering</li> </ul>	<ul style="list-style-type: none"> <li>CMOS logic</li> <li>Manufacturing</li> <li>Project management</li> <li>Semiconductor physics</li> </ul>	<ul style="list-style-type: none"> <li>Statistics</li> <li>C++</li> <li>VLSI design</li> </ul>	<ul style="list-style-type: none"> <li>Boise, ID</li> <li>Longmont, CO</li> </ul>
<b>Test and Probe</b>	<ul style="list-style-type: none"> <li>Test engineer</li> <li>Equipment engineer</li> <li>Process engineer</li> <li>Manufacturing engineer</li> <li>Software engineer</li> </ul>	<ul style="list-style-type: none"> <li>BS</li> <li>MS</li> </ul>	<ul style="list-style-type: none"> <li>Computer engineering</li> <li>Electrical engineering</li> <li>Industrial engineering</li> <li>Mechanical engineering</li> <li>Computer science</li> </ul>	<ul style="list-style-type: none"> <li>Digital logic</li> <li>Electrical/electronics/mechanics</li> <li>Operations/project management</li> <li>Systems engineering</li> </ul>	<ul style="list-style-type: none"> <li>Programming: Python, C++, Perl</li> <li>Statistics</li> <li>Design of experiments (DOE)</li> <li>UNIX</li> <li>CAD</li> </ul>	<ul style="list-style-type: none"> <li>Boise, ID</li> <li>Clay, NY (coming soon)</li> </ul>

Department	Job title(s)	Education	Degree	Preferred coursework	Job location(s)	
<b>Advanced Packaging Technology Development</b>	<ul style="list-style-type: none"> <li>• Package development engineer</li> <li>• Semiconductor packaging engineer</li> <li>• Process development engineer</li> <li>• Process intergration engineer</li> <li>• Package modeling</li> <li>• Substrate designer</li> </ul>	<ul style="list-style-type: none"> <li>• MS</li> <li>• PhD</li> </ul>	<ul style="list-style-type: none"> <li>• Chemistry/Chemical engineering</li> <li>• Electrical engineering</li> <li>• Microelectronics engineering</li> <li>• Materials science</li> <li>• Mechanical engineering</li> <li>• Mechatronics/Physics</li> <li>• Metallurgical engineering</li> <li>• Polymer science</li> </ul>	<ul style="list-style-type: none"> <li>• Semiconductor packaging</li> <li>• Device or processing research</li> <li>• Semiconductor processing lab</li> <li>• Data science application in engineering</li> <li>• Thermal modeling</li> <li>• Mechanical modeling</li> </ul>	<ul style="list-style-type: none"> <li>• Statistics</li> <li>• Design of experiments (DOE)</li> <li>• Computational fluid dynamics</li> <li>• Computational materials</li> <li>• Thermo-mechanical modeling</li> <li>• AI assisted modeling</li> </ul>	<ul style="list-style-type: none"> <li>• Boise, ID</li> </ul>
<b>HBM/ DRAM Engineering</b>	<ul style="list-style-type: none"> <li>• Design engineer</li> <li>• Product engineer</li> <li>• Verification/validation engineer</li> <li>• Layout engineer</li> </ul>	<ul style="list-style-type: none"> <li>• BS</li> <li>• MS</li> <li>• PhD</li> </ul>	<ul style="list-style-type: none"> <li>• Electrical engineering</li> <li>• Computer engineering</li> </ul>	At least one from: <ul style="list-style-type: none"> <li>• CMOS logic</li> <li>• Digital logic</li> <li>• VLSI design</li> <li>• Analog circuit design</li> <li>• RTL circuit design</li> </ul>	<ul style="list-style-type: none"> <li>• C++</li> <li>• C#</li> <li>• Semiconductor physics</li> </ul>	<ul style="list-style-type: none"> <li>• Atlanta, GA</li> <li>• Boise, ID</li> <li>• Folsom, CA</li> <li>• Richardson, TX</li> </ul>
<b>Firmware Development</b>	<ul style="list-style-type: none"> <li>• Firmware architect</li> <li>• Firmware developer</li> <li>• Firmware test architect</li> <li>• Firmware tester</li> </ul>	<ul style="list-style-type: none"> <li>• BS</li> <li>• MS</li> <li>• PhD</li> </ul>	<ul style="list-style-type: none"> <li>• Computer engineering</li> <li>• Electrical engineering</li> </ul>	<ul style="list-style-type: none"> <li>• Programming projects in C/C++</li> <li>• Digital design</li> <li>• Digital devices</li> <li>• Control theory</li> </ul>	<ul style="list-style-type: none"> <li>• Computer architectures</li> <li>• Digital signal processing</li> <li>• Real time systems</li> </ul>	<ul style="list-style-type: none"> <li>• Boise, ID</li> <li>• Folsom, CA,</li> <li>• San Jose, CA,</li> <li>• Longmont, CO</li> <li>• Minneapolis, MN</li> </ul>
<b>Nonvolatile Engineering</b>	<ul style="list-style-type: none"> <li>• Design engineer</li> <li>• Product engineer</li> <li>• Verification engineer</li> <li>• System engineer</li> <li>• Characterization engineer</li> <li>• Yield engineer</li> </ul>	<ul style="list-style-type: none"> <li>• BS</li> <li>• MS</li> <li>• PhD</li> </ul>	<ul style="list-style-type: none"> <li>• Computer engineering</li> <li>• Electrical engineering</li> </ul>	At least one from: <ul style="list-style-type: none"> <li>• CMOS logic</li> <li>• Digital logic</li> <li>• VLSI design</li> </ul>	<ul style="list-style-type: none"> <li>• Programming language: C++, C#, Java, etc.</li> <li>• Semiconductor physics</li> <li>• Analog circuits</li> </ul>	<ul style="list-style-type: none"> <li>• Boise, ID</li> <li>• San Jose, CA,</li> <li>• Longmont, CO</li> </ul>
<b>SSD Engineering</b>	<ul style="list-style-type: none"> <li>• IC design engineer</li> <li>• Verification/validation engineer</li> </ul>	<ul style="list-style-type: none"> <li>• BS</li> <li>• MS</li> <li>• PhD</li> </ul>	<ul style="list-style-type: none"> <li>• Computer engineering</li> <li>• Electrical engineering</li> </ul>	<ul style="list-style-type: none"> <li>• Computer architecture</li> <li>• VLSI design</li> </ul>	<ul style="list-style-type: none"> <li>• Digital logic design</li> <li>• Verilog/VHDL/ SystemVerilog</li> </ul>	<ul style="list-style-type: none"> <li>• Boise, ID</li> <li>• San Jose, CA</li> <li>• Longmont, CO</li> </ul>
<b>Manufacturing/ Operations</b>	<ul style="list-style-type: none"> <li>• Manufacturing engineer</li> <li>• Process engineer</li> <li>• Proce integration engineer</li> <li>• Equipment engineer</li> <li>• EHS engineer</li> <li>• Facilities engineer</li> <li>• Industrial engineer</li> </ul>	<ul style="list-style-type: none"> <li>• BS</li> <li>• MS</li> </ul>	<ul style="list-style-type: none"> <li>• Chemistry/Chemical engineering</li> <li>• Electrical engineering</li> <li>• Environmental engineering</li> <li>• Industrial engineering</li> <li>• Mechanical engineering</li> <li>• Microelectronics engineering</li> <li>• Material science</li> <li>• Supply chain management</li> <li>• Physics</li> <li>• Other STEM degrees</li> </ul>	<ul style="list-style-type: none"> <li>• Statistics</li> <li>• SPC</li> <li>• Electromechanical coursework (<i>for equipment engineer role</i>)</li> </ul>	<ul style="list-style-type: none"> <li>• Programming</li> <li>• Semi process lab</li> <li>• Semiconductor materials</li> <li>• Semiconductor physics</li> </ul>	<ul style="list-style-type: none"> <li>• Boise, ID</li> <li>• Manassas, VA</li> <li>• Clay, NY (<i>coming soon</i>)</li> </ul>
<b>Information Technology</b>	<ul style="list-style-type: none"> <li>• Software engineer</li> <li>• System manager</li> <li>• Database administrator</li> <li>• Business analyst</li> <li>• Project manager</li> <li>• Software developer</li> </ul>	<ul style="list-style-type: none"> <li>• BS</li> <li>• MS</li> </ul>	<ul style="list-style-type: none"> <li>• Computer engineering</li> <li>• Computer science</li> <li>• Computer information systems</li> <li>• Management information systems</li> </ul>	<ul style="list-style-type: none"> <li>• C++/C#, .Net, Python/ Angular</li> <li>• Cloud, Windows, Unix, Linux</li> </ul>	<ul style="list-style-type: none"> <li>• SQL</li> <li>• SSIS, SSRS</li> <li>• SAP/ABAP</li> </ul>	<ul style="list-style-type: none"> <li>• Boise, ID</li> </ul>

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